Determining the Fermi Thermal Shift of Silicon Semiconductor Wafers Using Temperature-Dependent Raman Microscopy



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Introduction

As semiconductor devices get smaller and their power densities increase, understanding temperature effects at the micro- and nanoscale becomes increasingly important. Temperature-dependent (TD) Raman microscopy is a powerful and non-destructive method that can probe the thermal and electronic properties of semiconductor materials with submicron spatial resolution.

Higher temperatures influence crucial properties of semiconductors, leading to increased conductivity, reduced resistance, and shifts in the Fermi level.¹ These properties are intimately linked to the performance of the final device. It is, therefore, essential to have sensitive and specific techniques for probing the properties of these materials at different temperatures.

In this Application Note, we demonstrate the power of TD Raman microscopy by investigating the temperature-induced Raman spectral changes in a silicon (Si) chip with an Edinburgh Instruments RM5 Confocal Raman Microscope equipped with a Linkam stage. We show that Raman microscopy can be used to track changes in the electrical properties of the material, such as Fano resonance and the Fermi level.

Materials and Methods

The sample analysed was a 10 mm x 10 mm chip from an ndoped Si wafer. The chip was placed in a Linkam THMS600 Heating and Cooling Stage, which allows for temperature control in a range between 78 K and 873 K.

The stage was fixed underneath the microscope of an RM5 equipped with a 50X 0.5 NA long-working distance objective, a 532 nm laser and a 2400 gr/mm diffraction grating, Figure 1, which allowed for very high spectral resolution to be achieved. Spectral band widths were calculated using a twice-smooth ed Lorentzian function in Ramade[®] software. The temperature of the stage was controlled using Ramade, and spectra were recorded between 173 K and 473 K in 25 K steps.



Figure 1. An n-doped Si wafer inside a Linkam THM600, fitted to an RM5

Determining the Fermi Thermal Shift of Si Using Raman Mode Widths

One of the most crucial properties of semiconductor materials is the Fermi level, E_{F} . Understanding how it is affected by doping and temperature is important for tailoring the conductivity of a semiconductor in various electronic applications. Here we will cover how TD Raman spectra contain information about E_{F} .

In band-structure theory, which describes the energy levels in a semiconductor, the E_F is a statistical quantity representing the energy at which the probability of an electron occupying the valence or conduction band is 50%. The position of E_F relative to the conduction band determines the material's conductivity.

Figure 2 illustrates how the relationship between $E_{\rm F}$ and temperature differs for intrinsic and extrinsic semiconductors. In a simple intrinsic semiconductor, one which has not been doped, the $E_{\rm F}$ is very dose to the halfway point between the two bands regardless of the temperature. This is because each electron promoted to the conduction band leaves a positive hole in the valence band.



Figure 2 . The relationship between the Fermi level, E_F, and temperature in (A) intrinsic, (B) *n*-doped, and (C) *p*-doped semiconductors.

In an n-doped extrinsic semiconductor, such as the Si chip used in this Application Note, the E_{F} lies between the donor level and the conduction band at 0 K and gradually tends towards an intrinsic semiconductor's E_{F} as the temperature increases. This indicates that as donor electrons are depleted, intrinsic

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electrons begin to contribute more to the electrical conductivity of the material. The reverse is true for a *p*-doped extrinsic semiconductor, meaning that E_F gradually increases from below the acceptor level with temperature.

TD Raman microscopy can be used to probe E_F in a semiconductor and its thermally induced shift in a relatively simple manner compared to traditional techniques such as X-ray or ultra-violet photoelectron spectroscopy. In both *p*- and *n*-doped Si, the asymmetrically broadened Raman spectral line width of the LO phonon mode contains information about the E_F position through a phenomenon known as Fano resonance.²³ This arises from the interference between discrete inter-band transitions and the electronic continuum.

Asymmetric Fano broadening varies with temperature because of changes in the E_F , which describes the position of the electronic continuum. Information about it is contained in the width of the LO phonon mode, Γ_{Total} (Equation 1), along with anharmonicity-induced broadening, which can be calculated using a Balkanski model.⁴

(1)
$$\Gamma_{Total} = \Gamma_{Fano} + \Gamma_{Anharmonicity}$$

The spectra in Figure 3A show Raman spectra recorded on an *n*-doped Si chip between 173 K and 473 K in 25 K steps. These spectra show that as the temperature increases, the intensity of the dominant LO phonon band in Si (*ca.* 520.5 cm⁻¹ at room temperature) decreases, and the position of the band gradually shifts to a lower wavenumber.

It is not immediately dear from the spectra alone that the width of the phonon mode is changing with temperature. However, when the spectra are normalised and converted to an image where the X-axis is Raman shift, the Y-axis is temperature, and the Z-axis is Raman intensity, Figure 3B, it can be observed that the phonon mode becomes gradually wider with increasing temperature.



173 K

520

Raman shift (cm-1)

530

540

550

0.2

473 k

510

(A)

Intensity

490

(B)

473

500

Figure 3. (A) Raman spectra recorded from an *n*-doped Si chip between 173 K and 473 K in 25K steps. (B) Temperature map of the same spectra normalised.

Figure 4 presents the variation in LO phonon mode width, measured via FWHM, for *n*-doped Si between 173 K and 473 K. At 173 K, the FWHM is 4.2 cm⁻¹, while at 473K, it increases significantly to 6.2 cm⁻¹. This increase in peak width in temperature has previously been assigned to increased Fano resonance over anharmonicity effects, and an increase in Γ_{Faro} is associated with a decrease in $E_{F.}^{-3}$ More information about how E_{F} can be calculated directly from the FWHM of the silicon LO phonon mode is available in Rani *et al.*³ Briefly, the E_{F} can be calculated using only Γ_{Faro} , which can be determined using a Fano line fit and removing the effects of $\Gamma_{Artamoricity}$, which are calculated independently using the Balkanski model.



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Figure 4. A plot showing the LO phonon FWHM for an *n*-doped Si chip against temperature. The linear regression reveals a strong correlation between the two variables.

This data shows that as the temperature of the *n*-doped Si chip is increased, the FWHM of the LO phonon mode increases because the material gradually begins to behave like an intrinsic semiconductor, and E_F decreases towards the midway point between the valence and conduction band.

In p-doped Si, the FWHM of the LO phonon decreases with temperature, which represents the E_F increasing towards the midway point between the two bands. Like in the previous section, this data is only meaningful when the collection volume on the Si chip is free from other effects that would broaden the Raman spectrum, such as strain and crystallinity differences.

This is not only a promising method for characterising the electronic properties of silicon; it is valid for any material where Fano resonance occurs, such as molybdenum disulfide, graphene, and cadmium telluride.^{2,5,6}

Conclusions

TD Raman microscopy is a powerful tool for probing the thermal and electronic properties of semiconductors. By monitoring the LO phonon mode in an *n*-doped Si chip from 173 K to 473 K, we demonstrated the ability of the technique to track key electronic properties like the Fermi level and Fano resonance.

References

- V. K. Khanna, in Extreme Temperature and Harsh-Environment Electronics: Physics, Technology and Applications, IOP Publishing, 2017.
- 2. C. I. Medel-Ruiz et al., Solid State Comm., 2020, 312, 113895.
- 3. C. Rani et al., Anal. Chem., 2022, 94, 1510-1514.
- 4. M. Balkanski et al., Phys. Rev. B, 1983, 28, 1928-2934.
- 5. M. Tanwar et al., ACS Phys. Chem. Au, 2022, 2, 417-422.
- 6. D. Yoon et al., Carbon, 2013, 61, 373-378.



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